

U.S. PTO
12/03/01

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APP. NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10003312	12/03/2001	327		2816	

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APPLICANTS: Itooh Katsuaki; Inaba Tsuneo; Akita Hironobu;

CONTINUING DATA VERIFIED

THIS APPLICATION IS A DIVISION OF 02/505,004 02/10/2000

FOR FOREIGN APPLICATIONS VERIFIED:
JAPAN 11-203574 02/17/99

Foreign priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no
35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no
Continued and Acknowledged Examiner's initiative	<input type="checkbox"/> yes <input type="checkbox"/> no

ATTORNEY DOCKET NO.

001701.00000

TITLE: Synchronizing circuit for generating a signal synchronizing with a clock signal

U.S. DEPT. OF COMMERCE/PAT. & TM-PTO-430L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Fig. Drwg.
			Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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